

REMARKS

Reconsideration and allowance of the present application are respectfully requested. Claims 1-3, 11-13, and 15-30 are currently pending in this application.

Regarding the 35 U.S.C. § 112, Second Paragraph, Rejection

Claims 25 is rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which Applicant regards as the invention. Claim 25 has been amended in this Response in a manner which is believed to satisfactorily address the antecedent basis issue identified by the Office Action. Accordingly, the Applicant respectfully requests that the § 112 rejection be withdrawn.

Regarding the 35 U.S.C. § 103 Rejections

Claims 1-9, 11-22, and 25-27 are rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Published Application No. 20040054689A1 to Salmonsens et al. (referred to below as "Salmonsens") in view of U.S. Patent No. 6,208,350 to Herrera (referred to below as "Herrera"). Applicant respectfully traverses this rejection for the following reasons.

Prior to addressing the rejection, it is believed that the Patent Office may benefit from a brief review of exemplary aspects of the invention disclosed in the specification. Of course, the detailed description in the specification does not limit the claims. Nevertheless, an understanding of certain salient features of the invention will help the Patent Office better appreciate the distinction between the claims (to be discussed below) and the applied references.

1 According to one exemplary implementation, the specification describes
2 functionality for performing multiple video processing tasks in a single operation, as
3 opposed to serially. For instance, methods, apparatuses, and computer readable media
4 are described for de-interlacing a principal video stream at the same time that at least one
5 video sub-stream is combined with the principal video stream. See page 4, lines 5-9 of
6 the specification. More specifically, the invention executes these two functions such that
7 they can be performed in a single memory read/write operation, rather than in multiple
8 passes. That is, whereas the system 200 shown in Fig. 2 requires at least two stages to
9 perform the de-interlacing and the compositing operations (requiring at least two reads
10 from memory), the system 300 shown in Fig. 3 requires only a single stage (e.g., a single
11 memory read/write transaction) (requiring, in one exemplary case, only a single read
12 from memory). See page 11, lines 17-23 of the specification. Performing these tasks in a
13 single transaction, as opposed to staggered serial transactions, reduces the bandwidth
14 requirements of the processing operation. See page 4, lines 10-12 of the specification.
15 For example, in one case, the technique described in the specification can achieve
16 approximately a 62% bandwidth saving compared to the case in which video processing
17 operations are performed as separate operations in series. See page 13, lines 14 and 15 of
18 the specification. One way of implementing the above-identified features is by providing
19 a graphics unit which receives input from texturing units, substantially in parallel,
20 associated with the principal video stream and the video sub-stream. See page 4, lines
21 14-17.

22 The primary reference, Salmonsens, describes an emulator interface that can
23 analyze content communications to determine supported content formats, determine the
24 format of presented content, and reformat or transcode the content to place the presented
25 content in the supported format (paragraph 0007). The video processing associated

1 within this emulation can involve: removing an arbitrary frame region for processing; de-
2 interlacing a video frame; enlarging or reducing video width or height; filtering for image
3 resizing; removing an arbitrary frame region for encoding; downsampling of video
4 width/height; video frame flipping or mirror imaging; gamma correcting; anti-aliasing;
5 and color manipulating (note, for example, the list enumerated in claim 18 of
6 Salmonsens).

7 The secondary reference, Herrera, discloses a video processing stage 14 (depicted
8 in Fig. 1) that includes three sub-stages. The following excerpt of Herrera describes the
9 third substage 21 (column 3, lines 14-26, with emphasis added):

10
11 The resulting YUV 4:2:2 and decoded sub-picture digital video signals are then provided
12 to the third sub-stage 21 of video processing stage 14 which the YUV 4:2:2 and decoded sub-
13 picture digital video signals are blended together in an alpha blend process 32 to produce a
14 translucent overlay, as described above and in detail in the DVD specification. Next, the blended
15 digital video signal is provided to a YUV-to-RGB conversion process 34, in which the blended
16 digital video signal is converted from a YUV format into a corresponding red-green-blue (RGB)
17 format. The resulting RGB digital video signal is then provided to an image scaling process 36, in
18 which the RGB digital video signal is scaled to a particular size for display.

19
20 Herrera describes an improvement where three operations performed in the video
21 processing stage 14 are allocated to a graphics unit: a motion compensation process 28; a
22 YUV 4:2:0-to-3:2:2 conversion process 30; and an alpha blending process 32. See
23 column 9, lines 49-60. These three operations are performed in series, as indicated, for
24 example, by the fact that Herrera identifies the blending process as the "final stage"
25 (column 16, line 66).

1 Neither Salmonsens nor Herrera, whether considered alone or in combination,
2 disclose the subject matter of the claims. For example, consider independent claim 1,
3 reproduced below in its entirety (with emphasis added):

4
5 1. A method for processing video data, comprising:

6 receiving a principal video stream from a source;

7 receiving a video sub-stream containing supplemental information associated with the
8 principal video stream;

9 *in a single stage operation, performing an operation on the principal video stream and*
10 *combining the principal video stream with the video sub-stream to produce processed data,*
11 *wherein the single stage operation requires only a single read transaction to perform the single*
12 *stage operation; and*

13 outputting the processed data.
14

15 As identified above, Salmonsens identifies a list of video processing operations
16 including: removing an arbitrary frame region for processing; de-interlacing a video
17 frame; enlarging or reducing video width or height; filtering for image resizing; removing
18 an arbitrary frame region for encoding; downsampling of video width/height; video frame
19 flipping or mirror imaging; gamma correcting; anti-aliasing; and color manipulating.
20 Further, Salmonsens states that an emulator 800 can "generate a plurality of subpictures
21 that overlay video for captions, sub-titles, karaoke, menus, and animation" (paragraph
22 0137). However, nowhere does Salmonsens disclose at least the element of claim 1 which
23 reads: "*in a single stage operation, performing an operation on the principal video*
24 *stream and combining the principal video stream with the video sub-stream to produce*
25 *processed data, wherein the single stage operation requires only a single read*

1 *transaction to perform the single stage operation.*" For instance, there is absolutely no
2 hint in Salmonsens that the generating of subpictures (described in paragraph No. 0137)
3 takes place in a single stage along with any other processing performed on a principal
4 video stream.

5 Recognizing the shortcomings of Salmonsens, the Office Action supplements the
6 rejection with Herrera. But Herrera fares no better in meeting the subject matter recited
7 in claim 1. For example, Herrera discloses a stage 14 that include a third substage 21.
8 Substage 21, in turn, includes alpha-blending 32, YUV-to-RGB conversion 34, and
9 image scaling 36. But the terms "stage" and "substage" in Herrera are not being used in
10 the same sense that "stage" is used in claim 1. To clarify this issue, claim 1 has been
11 amended to recite: *wherein the single stage operation requires only a single read*
12 *transaction to perform the single stage operation.* There is no indication in either
13 Salmonsens and Herrera that any video operations are coupled together in a single stage
14 that involves a single read transaction as claimed.

15 For example, as noted above, Herrera describes the operations in the stage 14 as
16 being performed in series, as indicated, for example, by Herrera's use of the word "next,"
17 "then," "final," etc. In a conventional video processing pipeline, the output of a first
18 operation serves as input to a second operation, and the output of the second operation
19 serves as input to a third operation, and so forth. Each input operation along the pipeline
20 conventionally requires a separate read transaction, where input data is retrieved from
21 memory and supplied to the processing unit which will perform the computations,
22 followed by a write transaction where the output results are written to memory. Hence,
23 by virtue of the fact that Herrera discloses a collection of operations performed in series,
24 it requires multiple read transactions to perform the single stage operation, not a single
25 read transaction as claimed.

Further evidence of the above conclusion can be found in column 16, lines 19-27 of Herrera. This portion recites converting planar YUV 4:2:0 to interleaved 4:2:2 via a texture mapping engine 110. The Y, U and V pictures are broken up into squares, and each square becomes a source texture, which is mapped to the destination 4:2:2 picture. There is no indication that this operation is combined with any other operation in a single stage (requiring a single read transaction). (Compare this, for example, with the exemplary implementation shown in Fig. 5 of the presentation specification, where, in addition to YUV texture units, a texture unit 508 is assigned to supply video sub-stream data.)

In addressing a related feature in claim 4 (which has now been canceled in favor of the new feature added to claim 1), the Office Action states (in paragraph No. 9 of the Office Action):

Herrera [sic] describes that the performing and the combination are performed in a single stage operation, and the operation is performed on the inputted DVD data stream (14, Figure 1; Col. 2, line 39-Col. 3, line 19). The inputted DVD data stream was inherently retrieved by a single call to memory.

This statement reflects a misunderstanding of what is being claimed. First, claim 1 recites two receiving operations, following by a single read transaction. If the Office Action interprets the claimed single read transaction as the reading of data from a DVD, then this leaves open the question of what the Office Action is interpreting as the initial two receiving steps recited in claim 1. In fact, the single read transaction recited in claim 1 is performed *after* the receiving operations, so it is improper for the Office Action to

1 interpret the single read transaction as an initial input of data from a DVD (or from any
2 other source).

3 Second, according to MPEP § 2112, "In relying upon the theory of inherency, the
4 examiner must provide a basis in fact and/or technical reasoning to reasonably support
5 the determination that the allegedly inherent characteristic necessarily flows from the
6 teachings of the applied prior art." *Ex parte Levy*, 17 USPQ2d 1461, 1464 (Bd. Pat. App.
7 & Inter. 1990) (emphasis in original). The fact that a certain result or characteristic may
8 occur or be present in the prior art is not sufficient to establish the inherency of that result
9 or characteristic. *In re Rijckaert*, 9 F.3d 1531, 1534, 28 USPQ2d 1955, 1957 (Fed. Cir.
10 1993). The Office Action has not demonstrated that any aggregation of operations in
11 Herrera's stage 14 *necessarily* require only a single read transaction. Indeed, as
12 explained above, the fact that Herrera describes that the operations are performed in
13 series suggests that a combination of operations would require multiple read transactions,
14 not a single read transaction.

15 Finally, there is no motivation to combine Salmonsens and Herrera. Salmonsens is
16 directed to emulator functionality to transcode video signals into a specified format.
17 Herrera is directed to a technique for delegating three specific operations to a graphics
18 accelerator. There is no indication that Salmonsens's system can benefit from Herrera's
19 solutions, and thus, nothing links these documents together other than Patent Office's
20 exercise of impermissible hindsight. The Office Action asserts that Herrera discloses
21 performing operations in a single stage, and that it would be obvious to apply this
22 functionality to Salmonsens in order to make the process faster. However, Herrera's
23 improvements in processing speed do not result from grouping operations together into a
24 single stage, but rather the allocation of the three enumerated operations to a graphics
25 accelerator. Hence, a practitioner interested in improving processing speed would not

1 look to Herrera's stage organization (shown in Herrera's Fig. 1) as model for modifying
2 Salmonsens.

3 As stated in MPEP § 2143 to establish a prima facie case of obviousness, three
4 basic criteria must be met. First, there must be some suggestion or motivation, either in
5 the references themselves or in the knowledge generally available to one of ordinary skill
6 in the art, to modify the reference or to combine reference teachings. Second, there must
7 be a reasonable expectation of success. Finally, the prior art reference (or references
8 when combined) must teach or suggest all the claim limitations. The Office Action fails
9 to set forth a prima facie case of obviousness for any one of these reasons. For example,
10 as to the third criterion, neither Salmonsens nor Herrera disclose, alone or in combination,
11 a single stage operation, wherein the single stage operation requires only a single read
12 transaction to perform the single stage operation. Indeed, neither Salmonsens nor Herrera
13 even address the basic motivation of combining *any video processing operations*
14 *together*. Herrera's Fig. 1 does not suggest that the substages shown there are combined
15 in the specific manner that is now recited in claim 1.

16 For at least the above-identified reasons, the Applicant submits that the
17 combination of Salmonsens and Herrera does not render claim 1 obvious under 35 U.S.C.
18 § 103. The remaining independent claims (11 and 25-27) have been amended to recite
19 related subject matter to claim 1, and are therefore allowable for reasons similar to those
20 provided above.

21 The remaining rejected claims (2-9 and 12-22) depend from the above-identified
22 independent claims, and are allowable for at least this reason. These claims also recite
23 additional features which are not disclosed in the either Salmonsens or Herrera, whether
24 considered alone or combination. For example, claim 2 recites that the performing of the
25 operation comprises de-interlacing the principal video stream. Salmonsens mentions de-

1 interlacing, which is not new by itself, of course. Herrera, on the other hand, actually
2 performs processing to *interlace* video information. Thus, even if, assuming *arguendo*,
3 one wanted to add Herrera to Salmonsens, Herrera would provide no suggestion to
4 integrate de-interlacing with any other operation, as Herrera is not concerned with de-
5 interlacing, but rather interlacing.

6 Claim 10 is rejected under 35 U.S.C. § 103(a) as being unpatentable over
7 Salmonsens and Herrera, and further in view of U.S. Patent No. 6,208,350 to MacInnis et
8 al. (referred to as "MacInnis"). Applicant respectfully traverses this rejection for the
9 following reasons.

10 Claim 1 depends from independent claim 1, and is therefore allowable for at least
11 this reason. MacInnis does not remedy the above-identified deficiencies of Salmonsens
12 and Herrera, whether these documents are considered alone or in any combination.
13 Accordingly, the Applicant requests that the § 103 rejection based on Salmonsens,
14 Herrera, and MacInnis be withdrawn.

15 Claims 23 and 24 are rejected under 35 U.S.C. § 103(a) as being unpatentable
16 over Salmonsens and Herrera, and further in view of U.S. Patent No. 6,466,226 to Watson
17 et al. (referred to as "Watson"). Applicant respectfully traverses this rejection for the
18 following reasons.

19 Claim 23 and 24 depend (directly or indirectly) from independent claim 11, and
20 are therefore allowable for at least this reason. Watson does not remedy the above-
21 identified deficiencies of Salmonsens and Herrera, whether these documents are
22 considered alone or in any combination.

23 In addition, Watson does not even disclose the subject matter recited in claims 23
24 and 24 for which it was cited. Consider claim 24, for instance, which recites that a data
25 processing module is configured to execute the performing and combination in a single

1 stage by processing video data obtained from a first and second texturing units
2 substantially in parallel. The portion of Watson identified in the Office Action discloses
3 (column 6, line 66 to column 7, line 7):

4
5 A texture palette 213, also known as a color lookup table (CLUT), may be provided
6 within GMCH 210 to identify a subset from a larger range of colors. A small number of colors in
7 the palette 215 allows fewer bits to be used to identify the color or intensity of each pixel. The
8 colors for the textures are identified as indices to the texture palette 215. In addition, a subpicture
9 palette 215 may separately be provided for color alpha-blending subpicture pixels for
10 transparency. . . .

11
12 Watson identifies the features 213 and 215 as being color lookup tables. These tables do
13 not provide video data (associated with the principal video stream and the video sub-
14 stream), but rather just palette information.

15 For at least these reasons, the Applicant requests that the § 103 rejection based on
16 Salmonsens, Herrera, and Watson be withdrawn.

17
18 *Newly Added Claims*

19 Claims 28-30 are new claims added herein. Claim 28 is an independent claim, as
20 reproduced below:

21
22 28. A method for processing video data, comprising:
23 receiving a principal video stream from a source;
24 receiving a video sub-stream containing supplemental information associated with the
25 principal video stream;

1 *in a single stage operation, performing an operation on the principal video stream and*
2 *combining the principal video stream with the video sub-stream to produce processed data,*
3 *wherein the single stage operation involves reading first input data associated with the received*
4 *principal video stream in parallel with second input data associated with the received video sub-*
5 *stream data; and*
6 outputting the processed data.

7
8 None of the applied documents disclose at least the claimed feature which recites:
9 “in a single stage operation, performing an operation on the principal video stream and
10 combining the principal video stream with the video sub-stream to produce processed
11 data, wherein the single stage operation involves reading first input data associated with
12 the received principal video stream in parallel with second input data associated with the
13 received video sub-stream data.” For example, there is no suggestion that any of the
14 enumerated operations shown in Fig. 1 of Herrera involve reading first input data and
15 second input data in parallel in the manner claimed. For at least this reason, the
16 Applicant submits that claim 28 is allowable over the applied art.

17 Claims 29 and 30 depend ultimately from independent claim 1, and are therefore
18 allowable for at least this reason.

19
20 *Conclusion*

21 The arguments presented above are not exhaustive; Applicant reserves the right to
22 present additional arguments to fortify its position. Further, Applicant reserves the right
23 to challenge the prior art status of one or more documents cited in the Office Action.

1 All objections and rejections raised in the Office Action having been addressed, it
2 is respectfully submitted that the present application is in condition for allowance and
3 such allowance is respectfully solicited. The Examiner is urged to contact the
4 undersigned if any issues remain unresolved by this Amendment.
5
6

7 Respectfully Submitted,
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